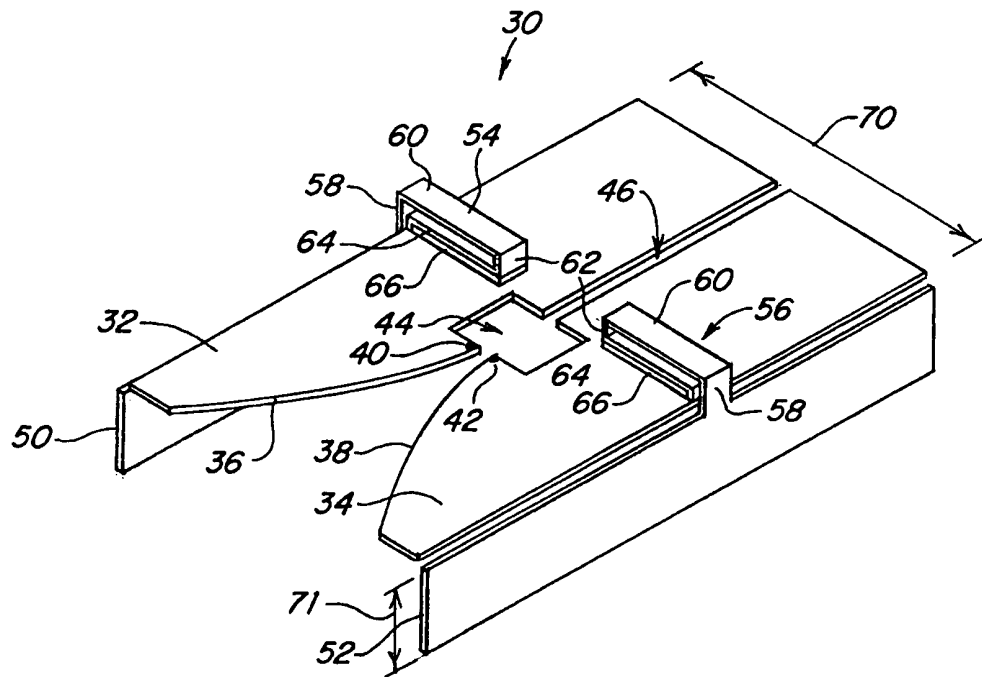
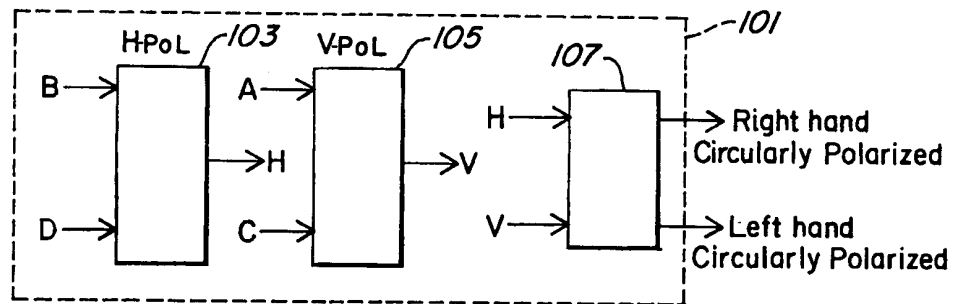
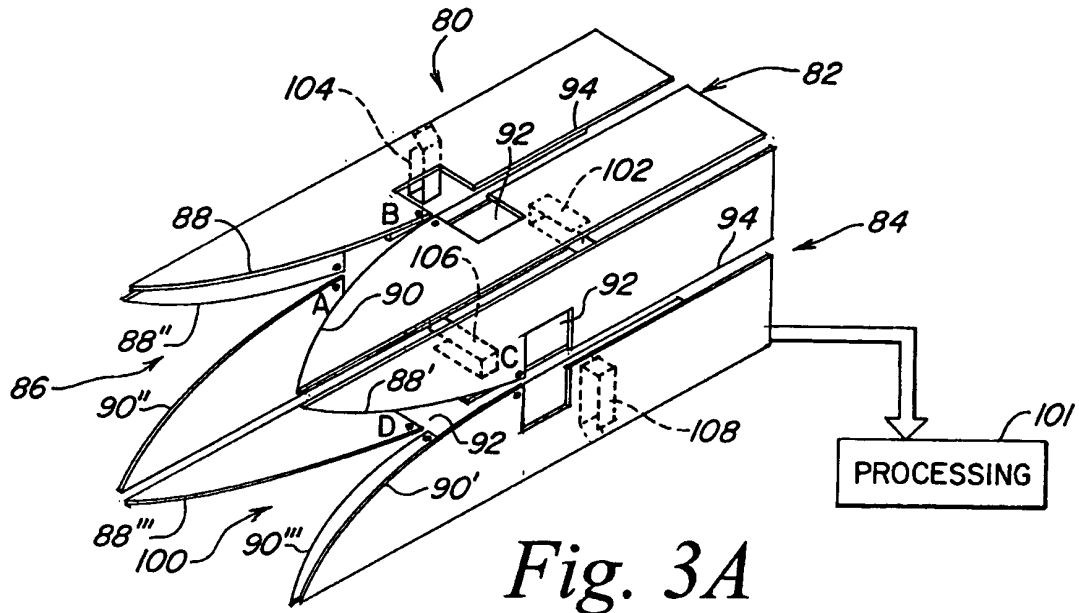


*Fig. 1*



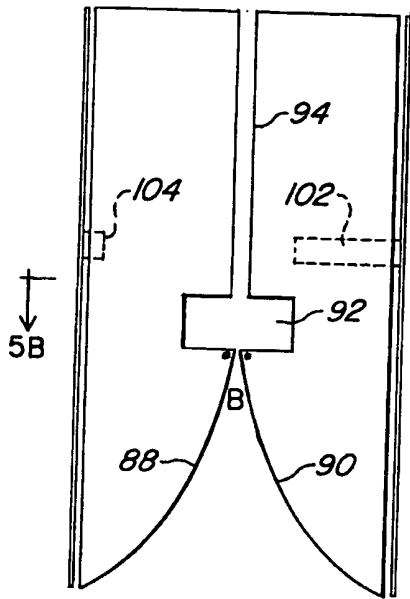
*Fig. 2*



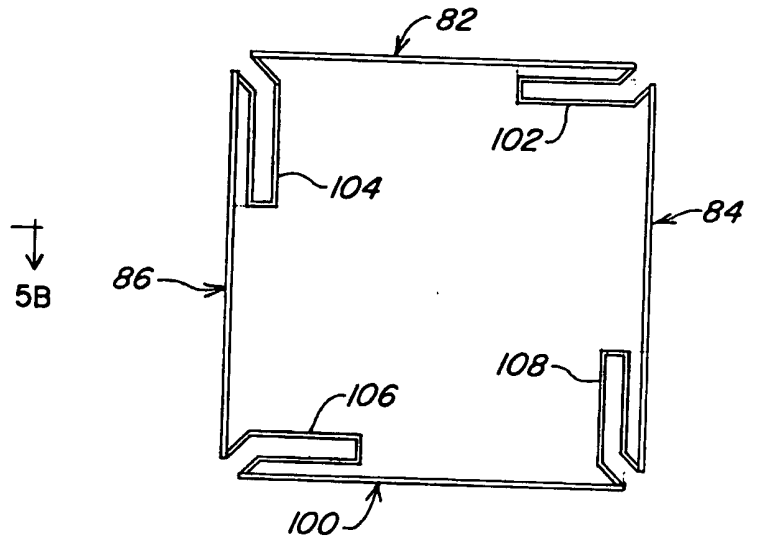
Mode Table

	VPoL	HPoL	RH C-PoL	LH C-PoL
A	1	0	1	1
B	0	1	-i	+i
C	1	0	1	1
D	0	1	-i	+i

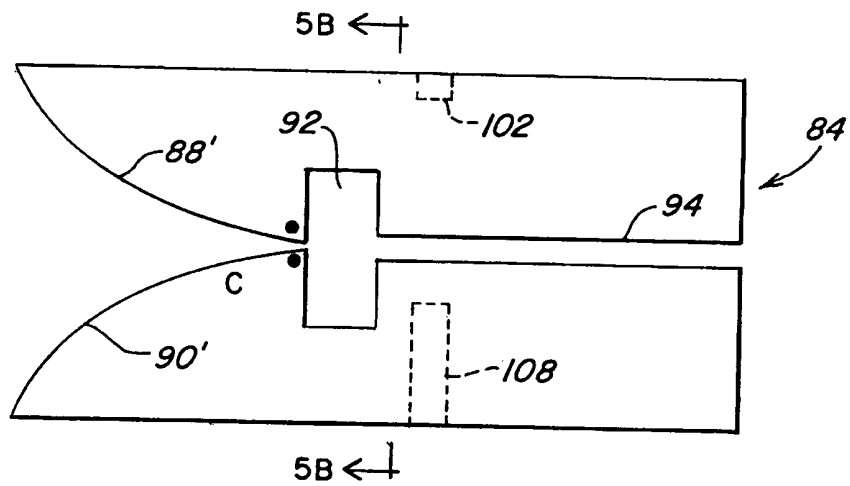
*Fig. 4*



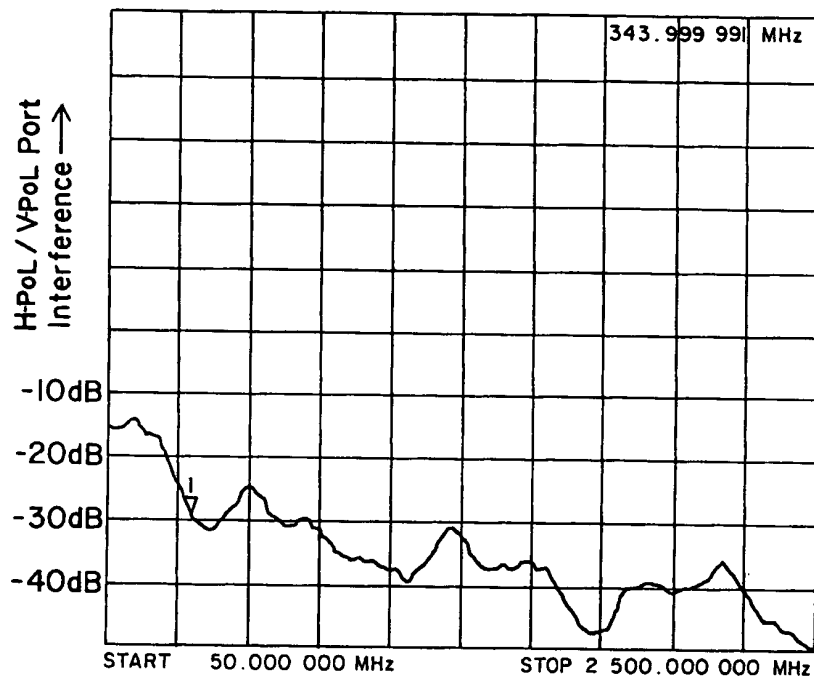
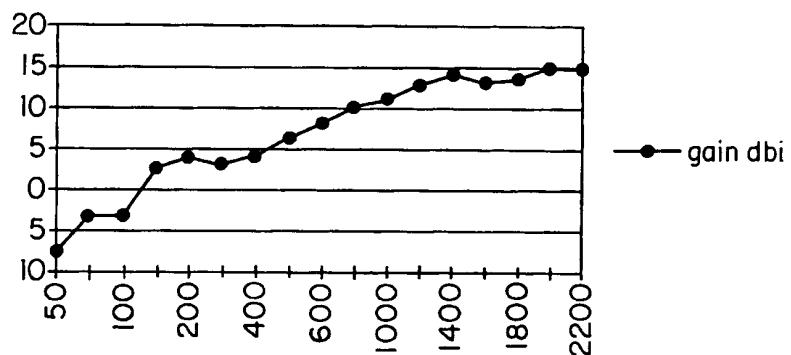
*Fig. 5A*

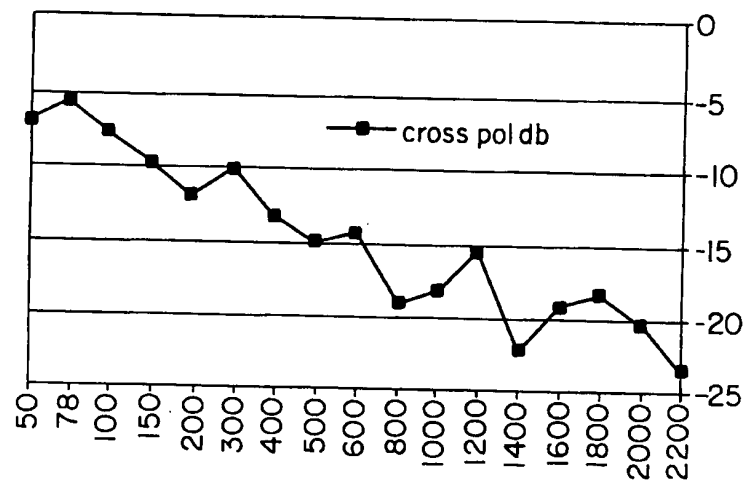


*Fig. 5B*



*Fig. 5C*

*Fig. 6**Fig. 7*

*Fig. 8*